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				Application Number	10/722583	
			ISCLOSURE	Filing Date	12-5-03	
			APPLICANT	First Named Inventor	SUNTER, Stephen K.	
			, _	Group Art Unit	2133	
			ts as necessary)	Examiner Name	BRITT	
Sheet	1	of	1	Attorney Docket Number	LVPAT065US	

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ab		CAI et al., "Jitter Testing for Gigabit Serial Communication Transceivers", Design & Test, JanFeb. 2003, p. 66-74, IEEE Circuits & Systems Society, USA.			
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Application Number	10/727,583			
Filing Date	12/04/2003			
First Named Inventor	Stephen K. SUNTER			
Group Art Unit	9858 2133			
Examiner Name ·	NTA BLITT			
Attorney Docket Number	LVPAT065US			

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Examiner Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²		
C6		BURNS ET AL., "An Introduction to Mixed-Signal IC Test and Measurement", February 8, 2001, p. 399-400, Oxford University Press.			
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Examiner	1 the	Ber AA	Date	7-26-04
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